

# WEST Search History

Hide Items

Restore

Clear

Cancel

DATE: Sunday, March 29, 2009

**Hide?** **Set Name** **Query**

**Hit Count**

*DB=PGPB,USPT; PLUR=NO; OP=ADJ*

<input type="checkbox"/>	L74	L72 and (index\$3 or indirect\$2)	13
<input type="checkbox"/>	L73	accumulator\$1 and L72	6
<input type="checkbox"/>	L72	5526501.uref.	16
<input type="checkbox"/>	L71	L70 not L64	9
<input type="checkbox"/>	L70	simd and L68	9
<input type="checkbox"/>	L69	(L34 or L35) and L68	0
<input type="checkbox"/>	L68	(index\$3 or indirect\$2) with L67	137
<input type="checkbox"/>	L67	coefficient table\$1	2202
<input type="checkbox"/>	L66	L64 and L60	4
<input type="checkbox"/>	L65	L64 not L60	4
<input type="checkbox"/>	L64	simd and L63	8
<input type="checkbox"/>	L63	L59 with (index\$3 or indirect\$2)	64
<input type="checkbox"/>	L62	L61 not L60	78
<input type="checkbox"/>	L61	simd and L59	84
<input type="checkbox"/>	L60	(L34 or L35) and L59	7
<input type="checkbox"/>	L59	coefficient memory	2313
<input type="checkbox"/>	L58	indirect\$2 and L57	1
<input type="checkbox"/>	L57	coefficient\$1 and L55	3
<input type="checkbox"/>	L56	L55 not L54	3
<input type="checkbox"/>	L55	L33 and simd	3
<input type="checkbox"/>	L54	L33 and L34	3

*DB=USPT; PLUR=NO; OP=ADJ*

<input type="checkbox"/>	L53	coefficient\$1 and L50	0
--------------------------	-----	------------------------	---

<input type="checkbox"/>	L52	indirect\$2 and L50	0
<input type="checkbox"/>	L51	L36 and L50	0
<input type="checkbox"/>	L50	L33 and L34	3
<input type="checkbox"/>	L49	L33 and l2L16	0
<input type="checkbox"/>	L48	5526501.pn.	1
<i>DB=EPAB; PLUR=NO; OP=ADJ</i>			
<input type="checkbox"/>	L47	systemonic.as. and 2003.py.	18
<input type="checkbox"/>	L46	simd and L45	2
<input type="checkbox"/>	L45	1995.py. and L43	132
<input type="checkbox"/>	L44	1996.py. and L43	111
<input type="checkbox"/>	L43	(hughes aircraft).as.	1609
<input type="checkbox"/>	L42	1995.py and (hughes aircraft).as.	0
<input type="checkbox"/>	L41	ep000638868.pn.	0
<input type="checkbox"/>	L40	ep95638868.ap.	0
<input type="checkbox"/>	L39	ep950638868.ap.	0
<input type="checkbox"/>	L38	1995.py. and hughes.as.	199
<i>DB=PGPB,USPT; PLUR=NO; OP=ADJ</i>			
<input type="checkbox"/>	L37	L34 and L36	51
<input type="checkbox"/>	L36	indirect\$2 with address\$4	8764
<input type="checkbox"/>	L35	(712/20).cccls.	164
<input type="checkbox"/>	L34	(712/22).cccls.	410
<input type="checkbox"/>	L33	alu\$1 same flag\$1 same port\$1 same (multiplexer\$1 or multiplexor\$1 or multi-plexer\$1 or multiplexor\$1)	81
<input type="checkbox"/>	L32	header\$1 with concatenat\$3 with data	683
<input type="checkbox"/>	L31	switch header\$1 with concatenat\$3 with data	1
<input type="checkbox"/>	L30	routing information with concatenat\$3 with data	6
<input type="checkbox"/>	L29	multiplex\$3 with L28	1
<input type="checkbox"/>	L28	routing with concatenat\$3 with data	94
<input type="checkbox"/>	L27	l24 not l25	40
<input type="checkbox"/>	L26	L25 not l21	27
<input type="checkbox"/>	L25	l22 with multiplex\$3	31

<input type="checkbox"/>	L24	l22 same multiplex\$3	71
<input type="checkbox"/>	L23	coefficient with L22	2
<input type="checkbox"/>	L22	concatenat\$3 with address with data	926
<input type="checkbox"/>	L21	l20 with data with concatenat\$3	11
<input type="checkbox"/>	L20	control signal\$1 with (multiplex\$3)	28220
<input type="checkbox"/>	L19	l14 with append\$3	0
<input type="checkbox"/>	L18	l14 with merg\$3	5
<input type="checkbox"/>	L17	l14 with join\$3	0
<input type="checkbox"/>	L16	l14 same concatenat\$3	7
<input type="checkbox"/>	L15	l14 with concatenat\$3	1
<input type="checkbox"/>	L14	L13 with data	1652
<input type="checkbox"/>	L13	(mux control\$1 or multiplexer control\$1 or multiplexor control\$1)	5933
<input type="checkbox"/>	L12	(mux control\$1 or multiplexer control\$1 or multiplexor control\$1) with coefficient data	0
<input type="checkbox"/>	L11	l7 same instruction\$1	34
<input type="checkbox"/>	L10	(l1 or l2) same (l8 or l9)	5
<input type="checkbox"/>	L9	index multiplexor\$1	17
<input type="checkbox"/>	L8	index multiplexer\$1	105
<input type="checkbox"/>	L7	(l1 or l2) same (l5 or l6)	252
<input type="checkbox"/>	L6	index with multiplexer\$1	2190
<input type="checkbox"/>	L5	index with multiplexor\$1	389
<input type="checkbox"/>	L4	(l1 or l2) same L3	4
<input type="checkbox"/>	L3	index multiplexor\$1	17
<input type="checkbox"/>	L2	input multiplexor\$1	1949
<input type="checkbox"/>	L1	input multiplexer\$1	21001

END OF SEARCH HISTORY